

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/054,653 Confirmation No.: 9448  
 First Named Inventor: Bulucea, Constantin Filing Date: 18 January 2002  
 Group Art Unit: 2814 Examiner: Farahani, D.  
 Atty. Docket No.: NS-5127 US  
 Title: Gate-Enhanced Junction Varactor With Gradual Capacitance Variation  
 Assignee(s): National Semiconductor Corporation

Mountain View, California  
 10 June 2003

BOX PATENT APPLICATION  
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Sir:

The undersigned attorney of record in the above identified patent application hereby authorizes and grants to TERRY KANNOFSKY / KATHY KANNOFSKY / AMBER CLICK and JAMES KANNOFSKY, all of TK Associates, Inc., 2001 Jefferson Davis Highway, Suite 300, Arlington, Virginia 22202, the power to inspect the above application and to make copies of documents in the file for this application.

Please telephone Attorney for Applicant(s) at 650-964-9767 if there are any questions in regard to this power to inspect and copy.

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